

FIG. 1

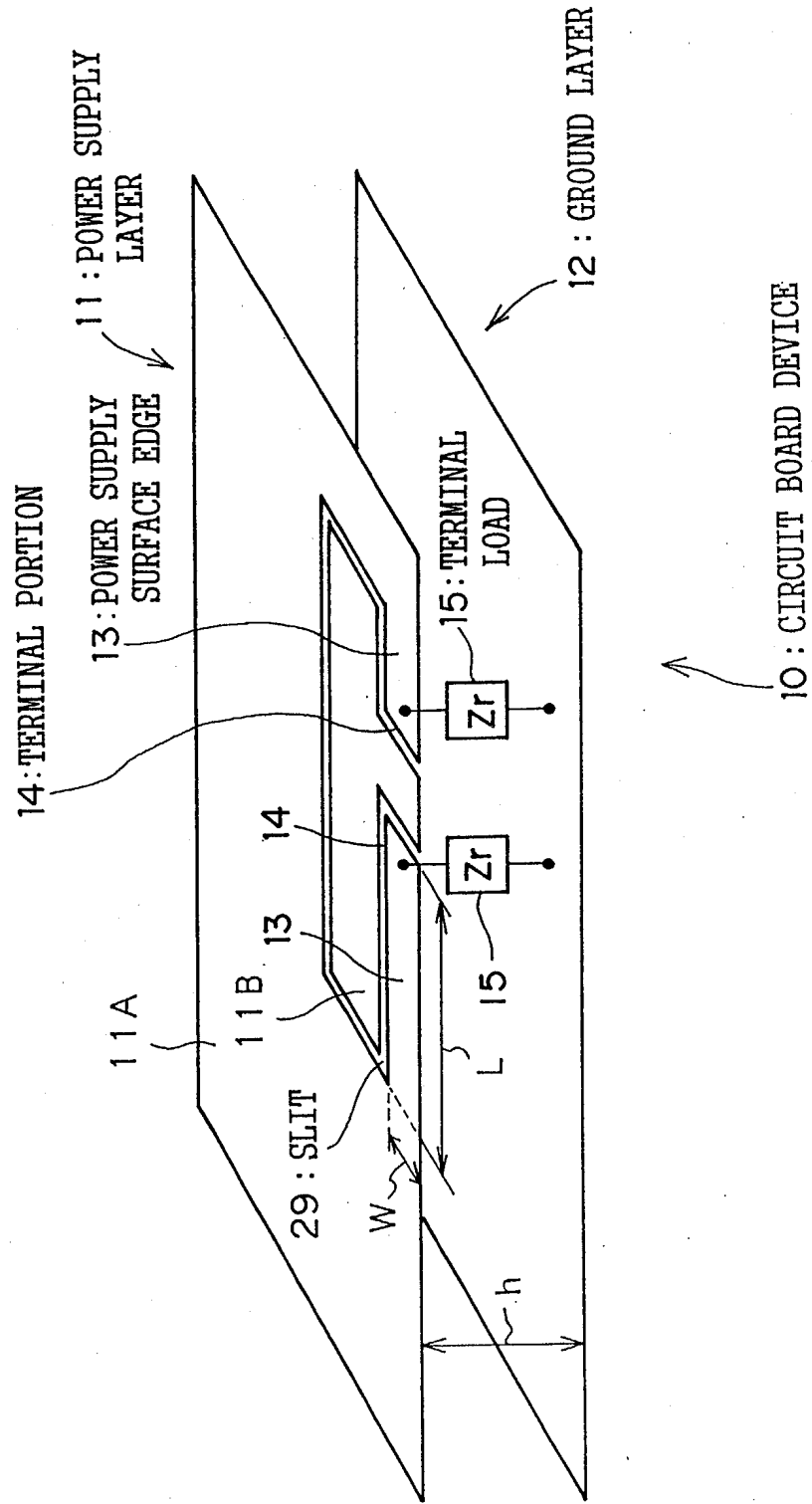


FIG. 2

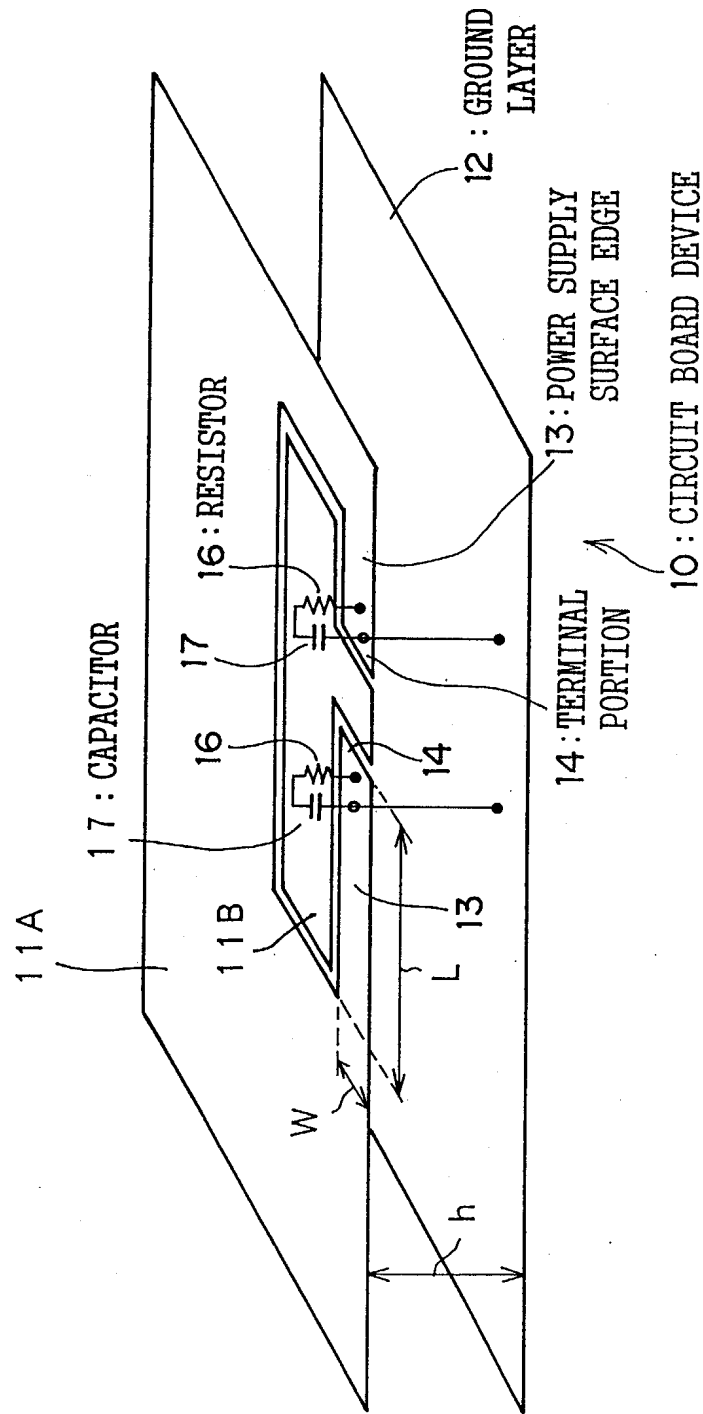


FIG. 3A

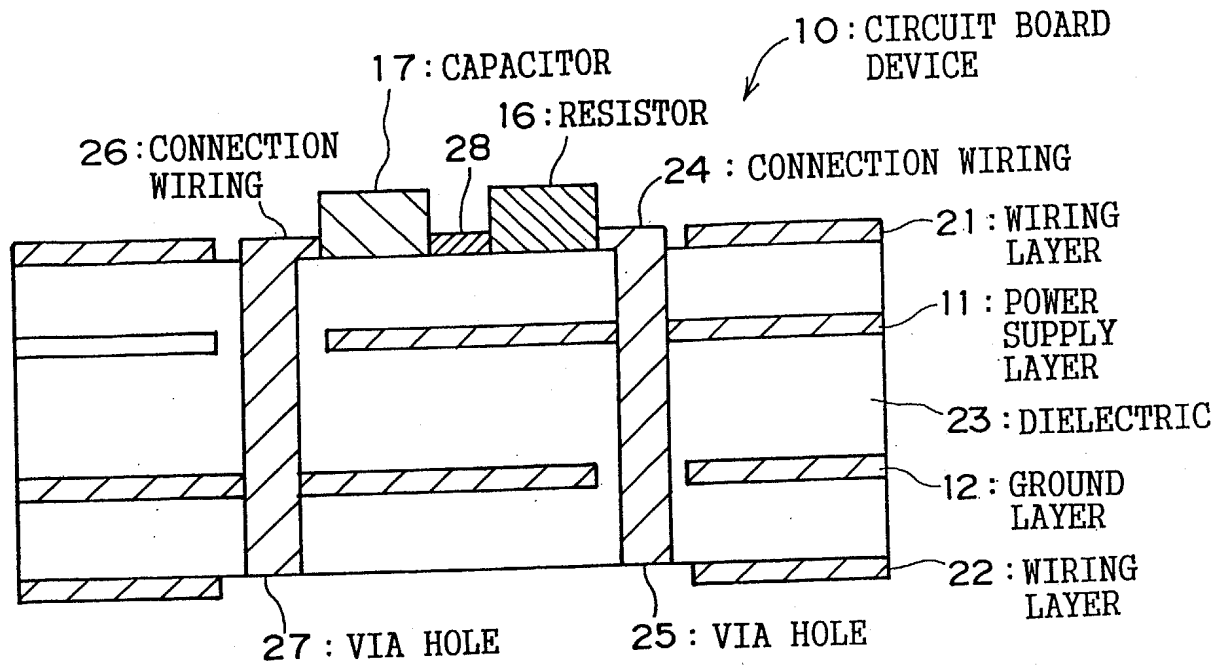


FIG. 3B

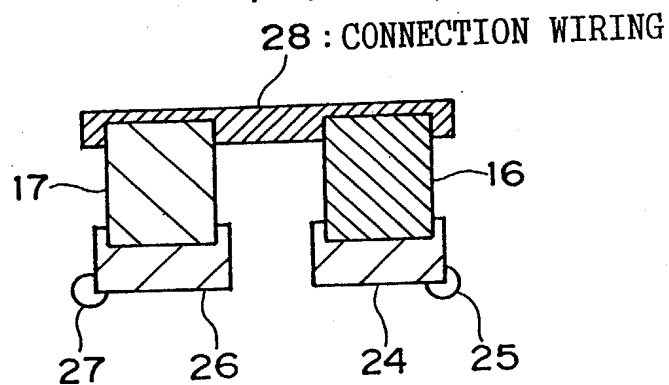


FIG. 3C

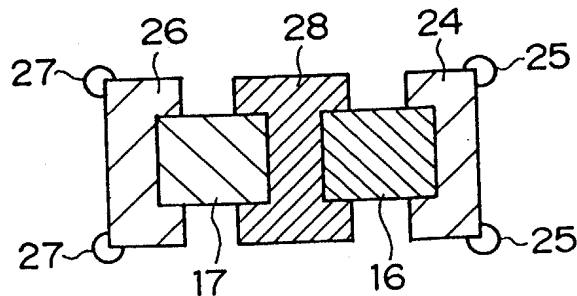


FIG. 4A

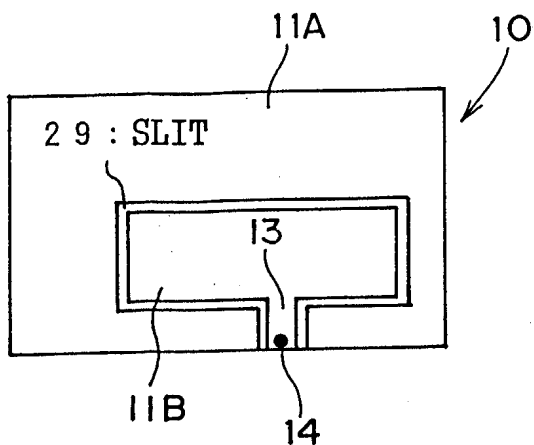


FIG. 4B

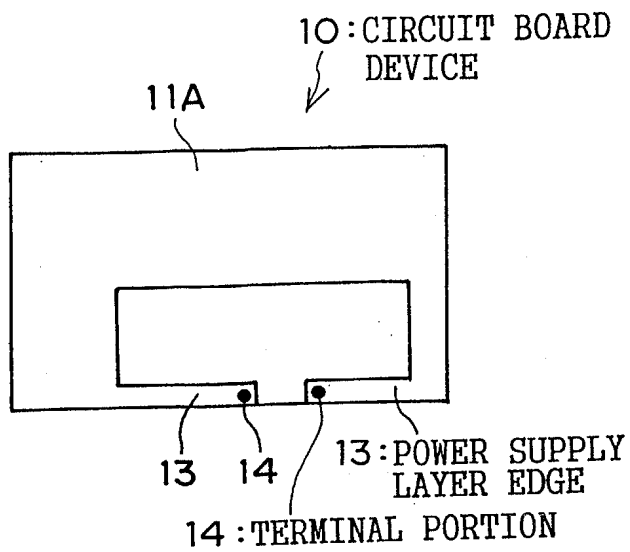


FIG. 4C

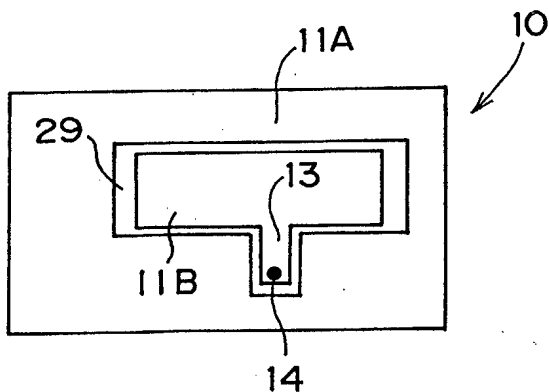


FIG. 4D

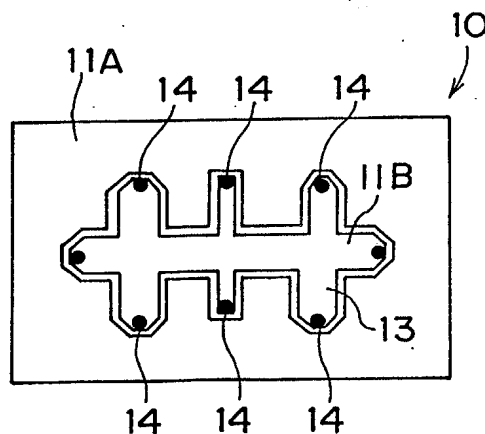
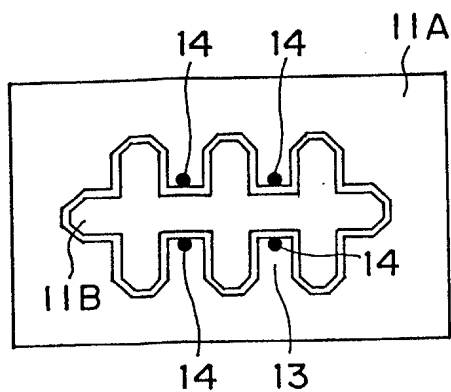


FIG. 4E



F I G. 5

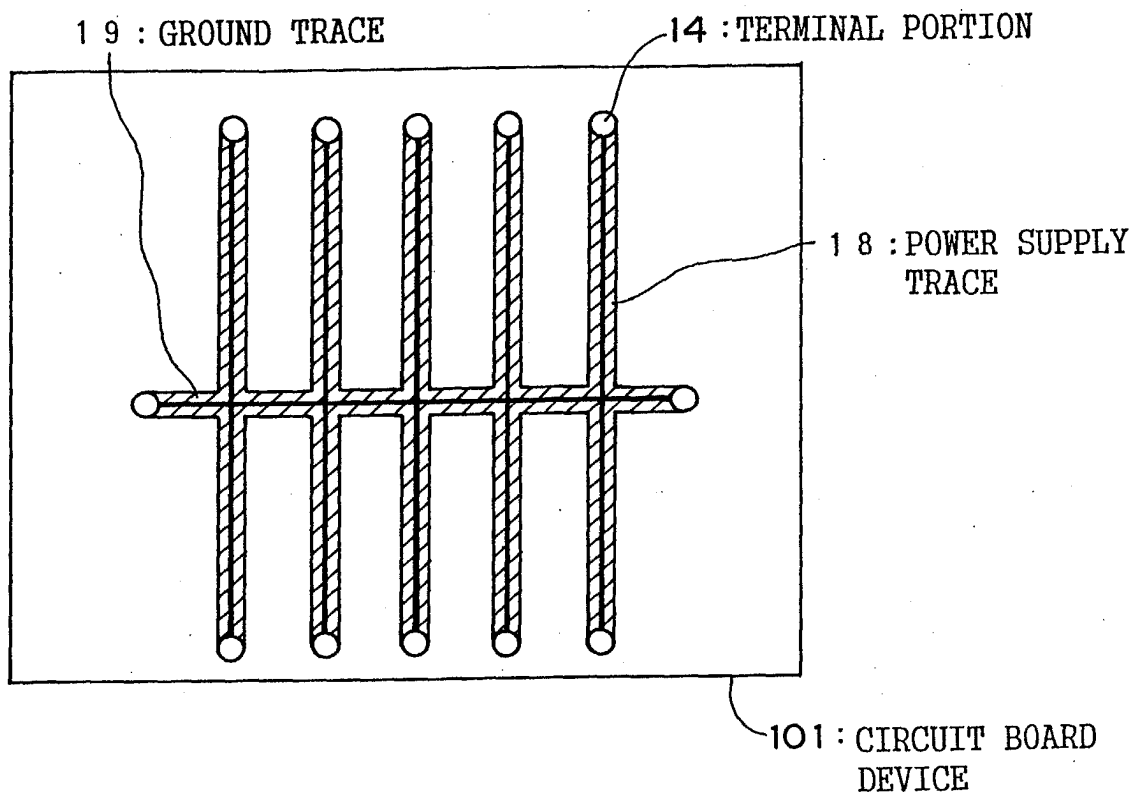


FIG. 6 A

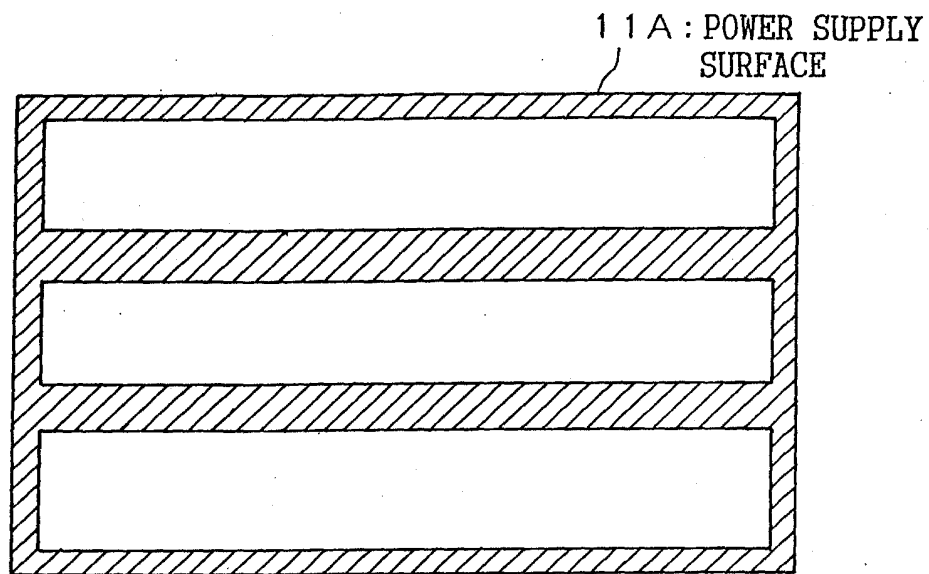


FIG. 6 B

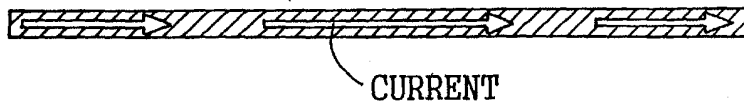


FIG. 7 A

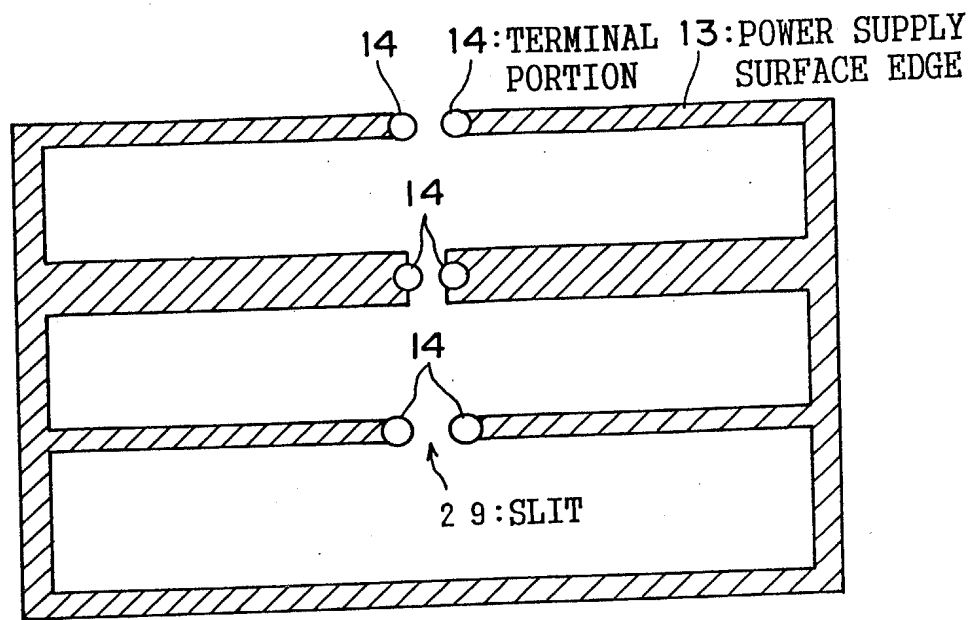


FIG. 7 B

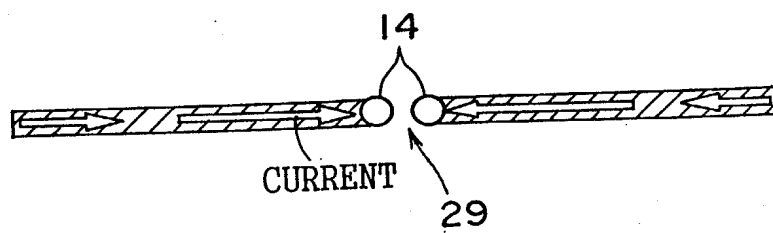


FIG. 8

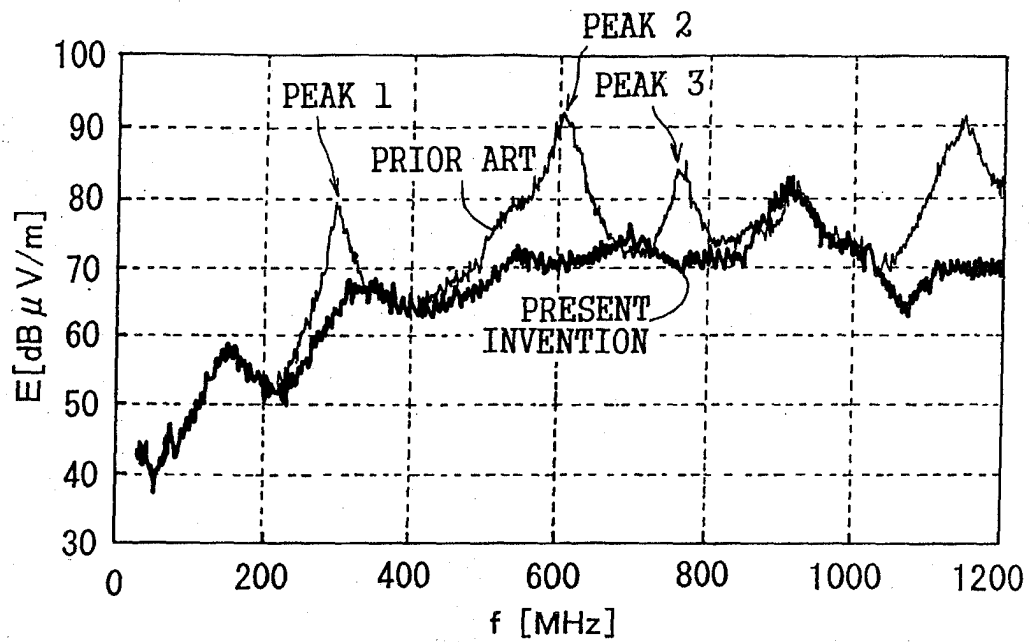


FIG. 9

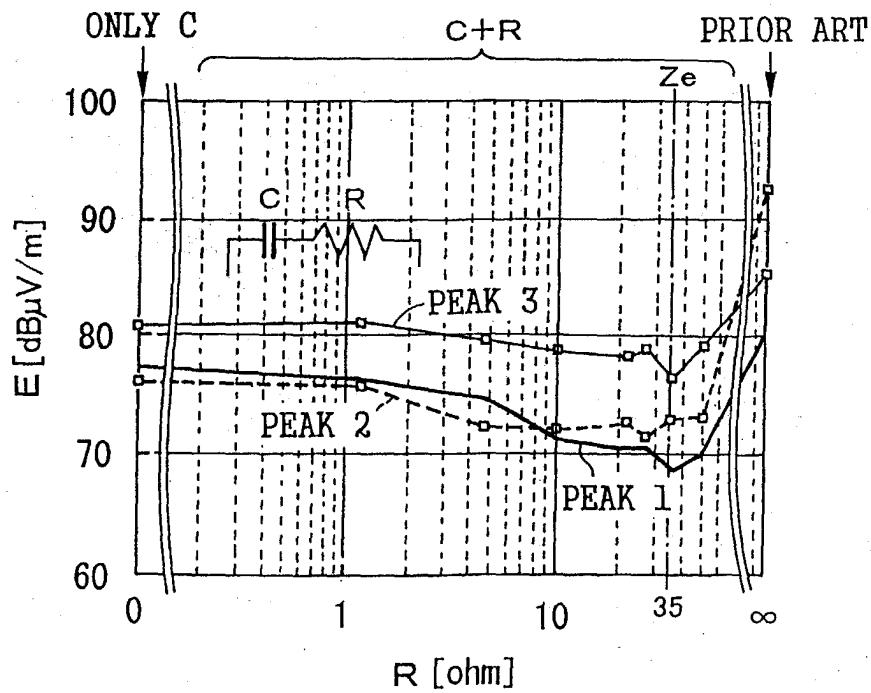


FIG. 10

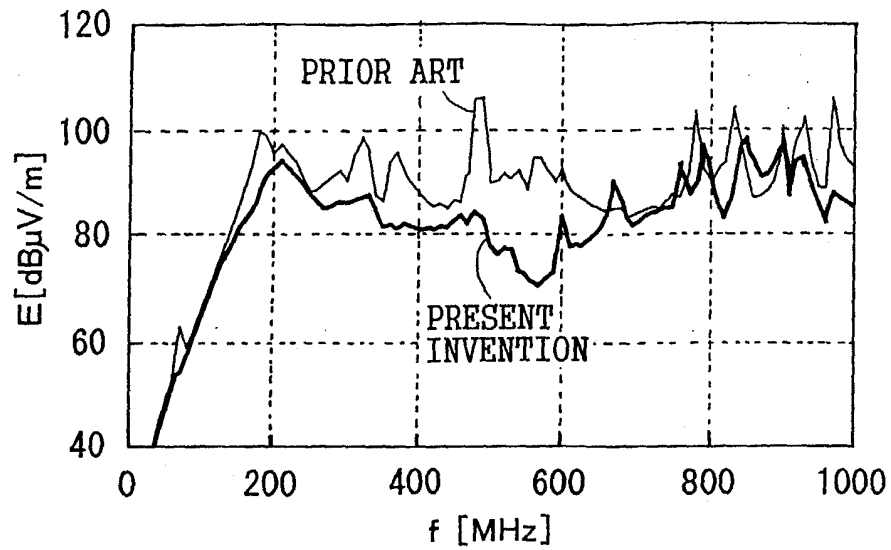
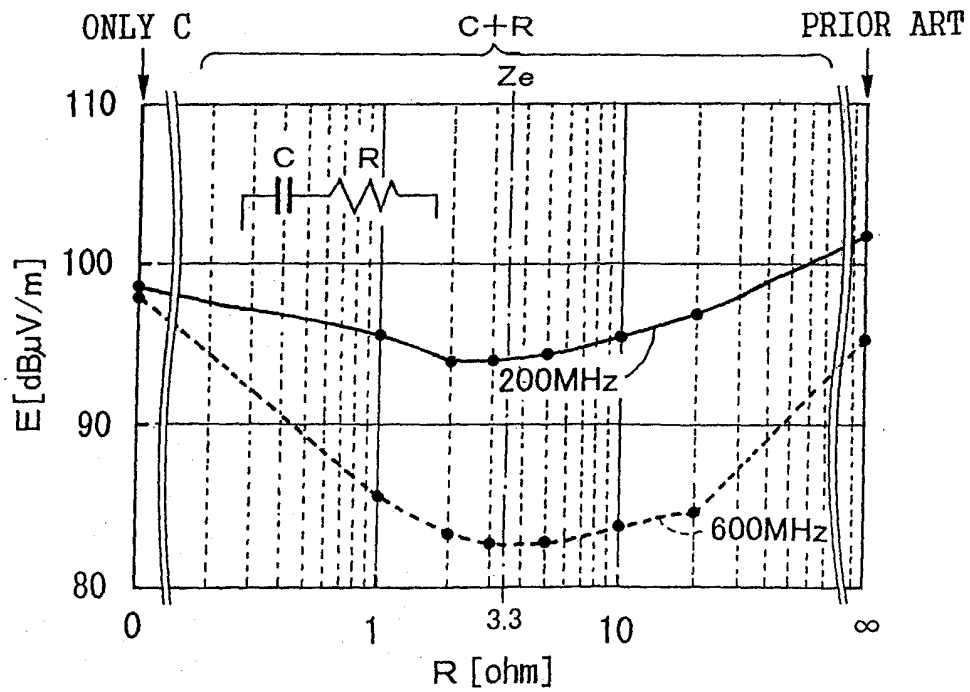


FIG. 11



F I G . 1 2

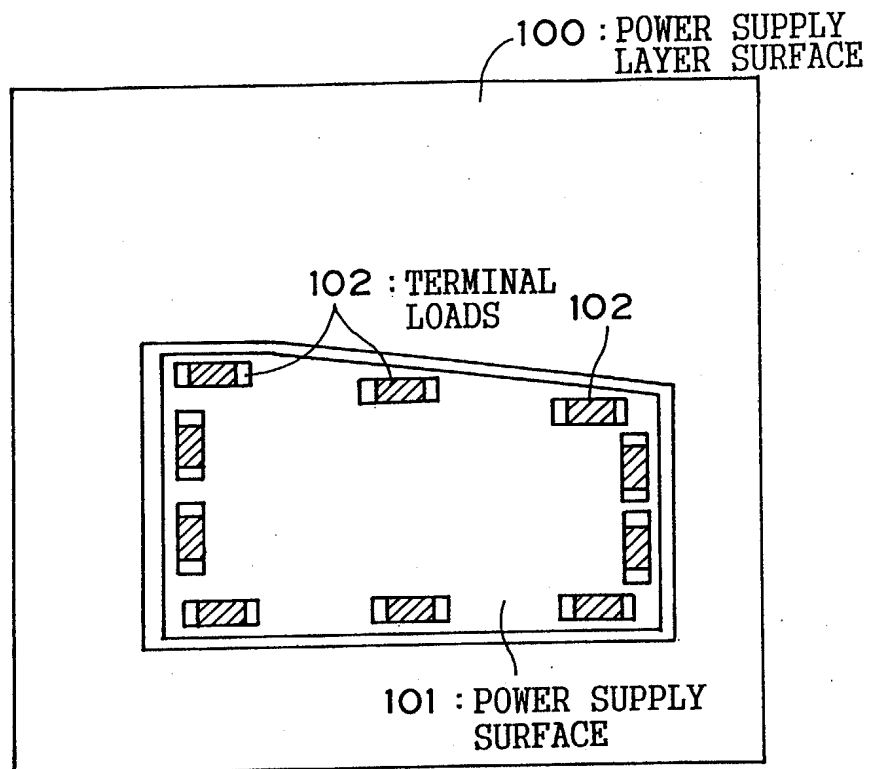


FIG. 13

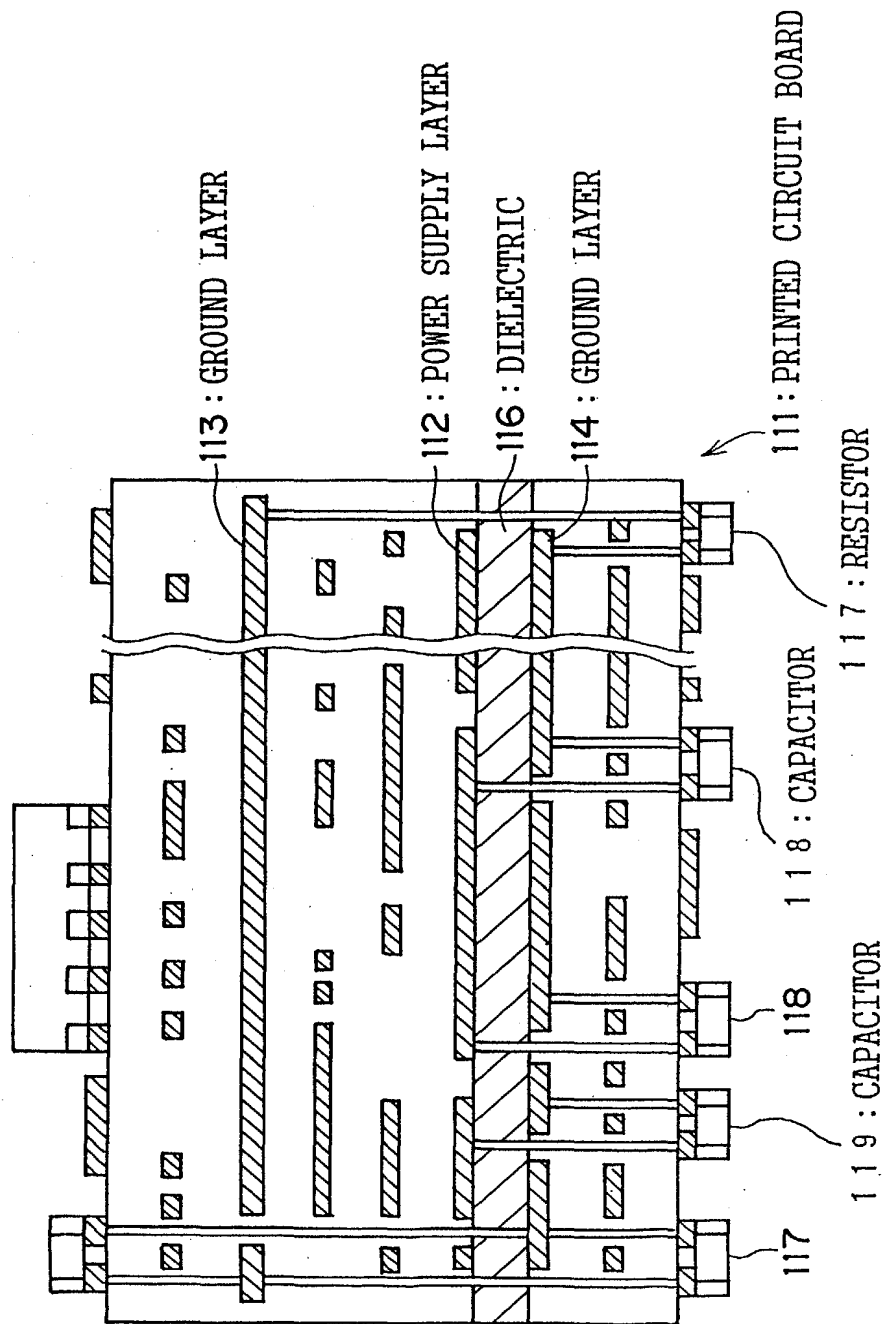


FIG. 14 A

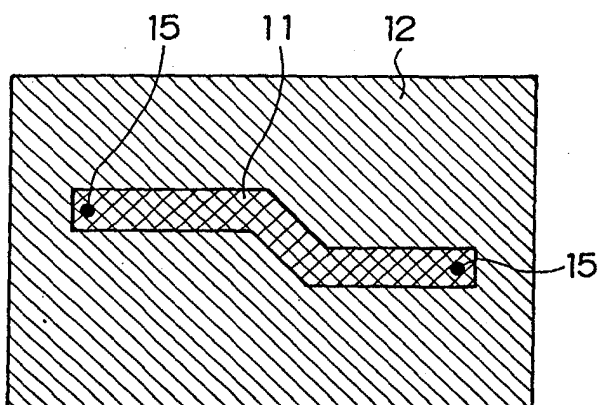


FIG. 14 B

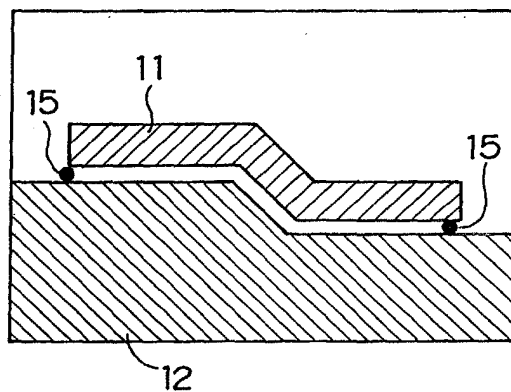


FIG. 14 C

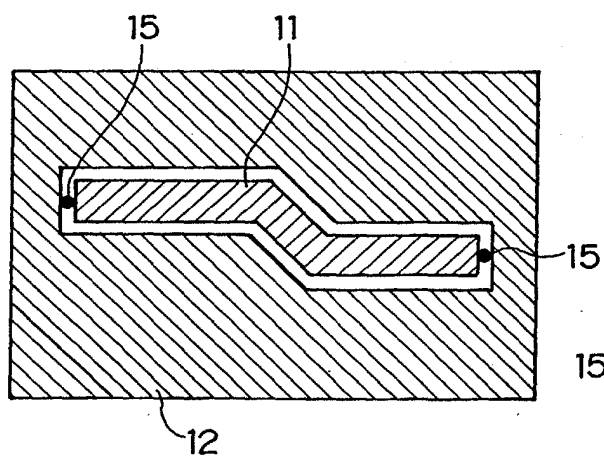


FIG. 14 D

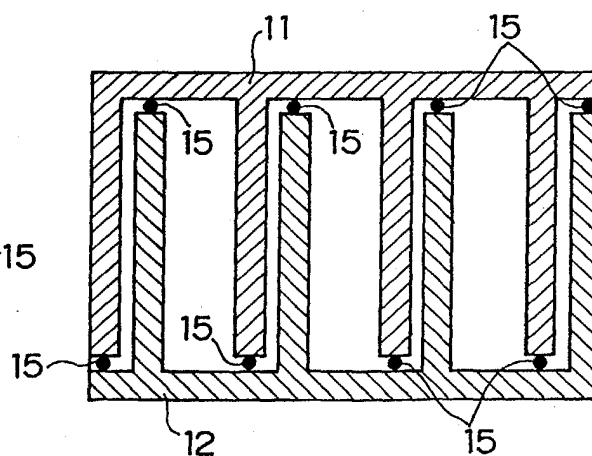


FIG. 15 A

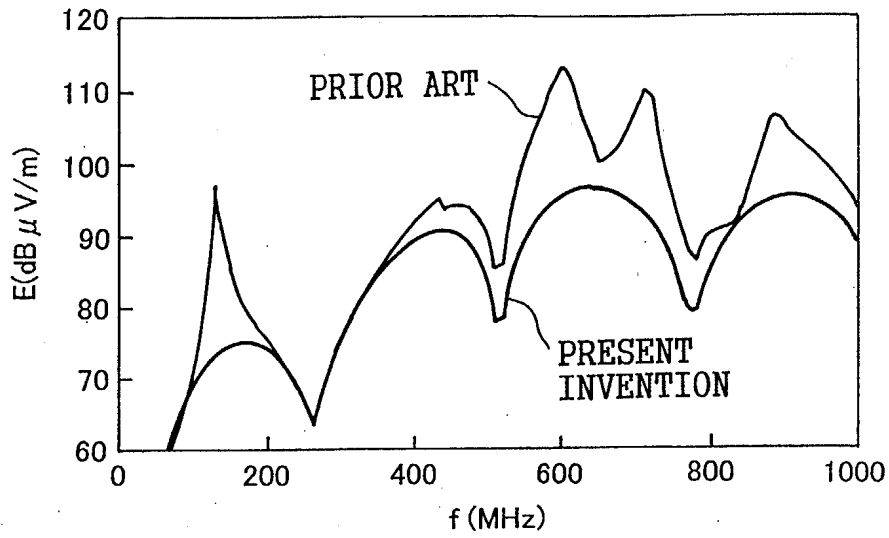


FIG. 15 B

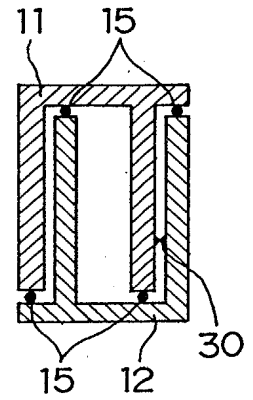


FIG. 16

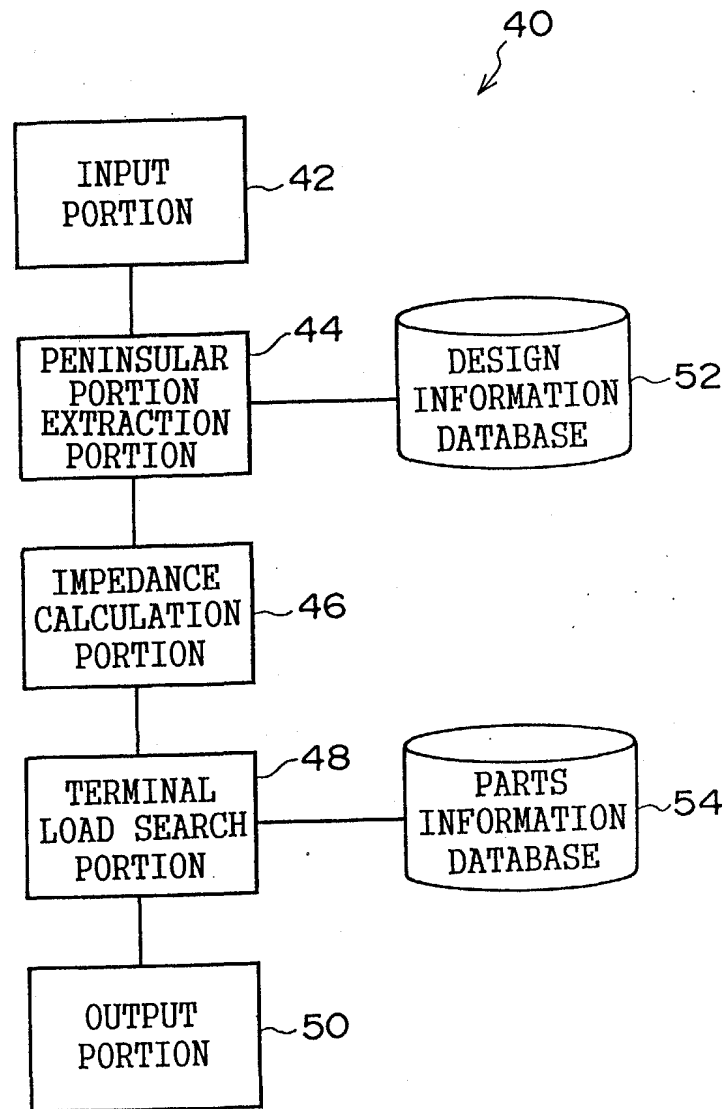


FIG. 17

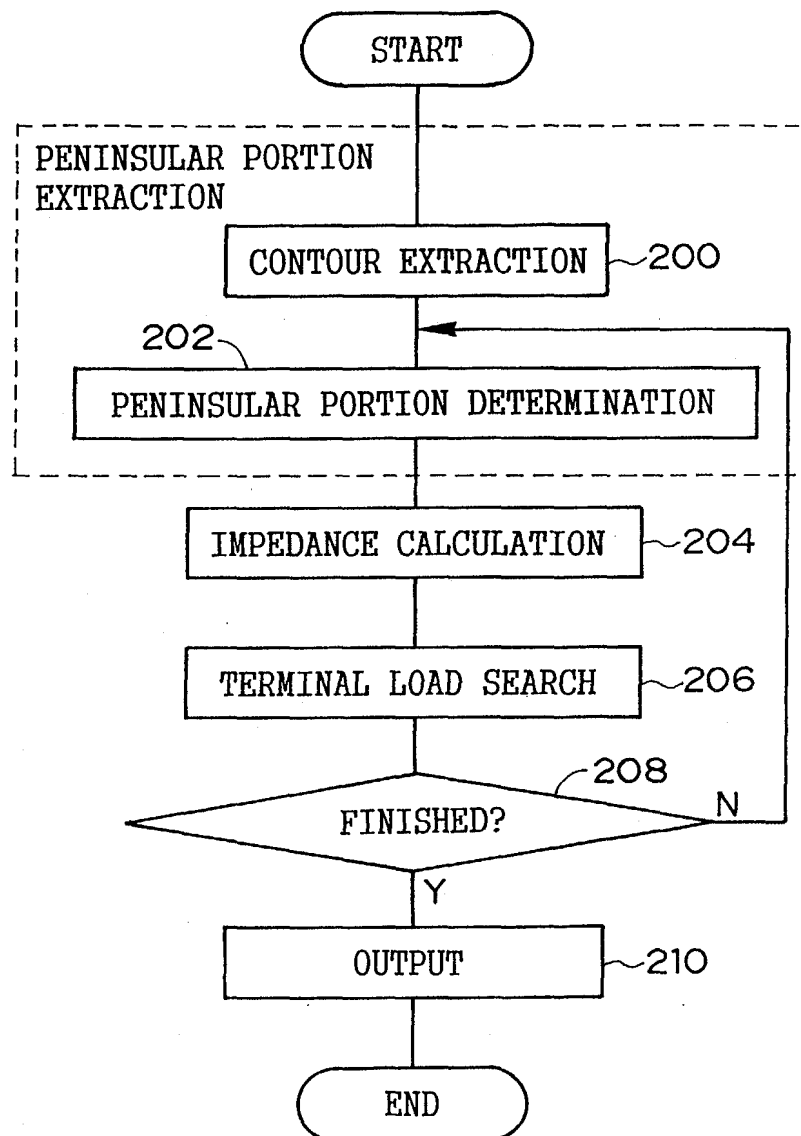
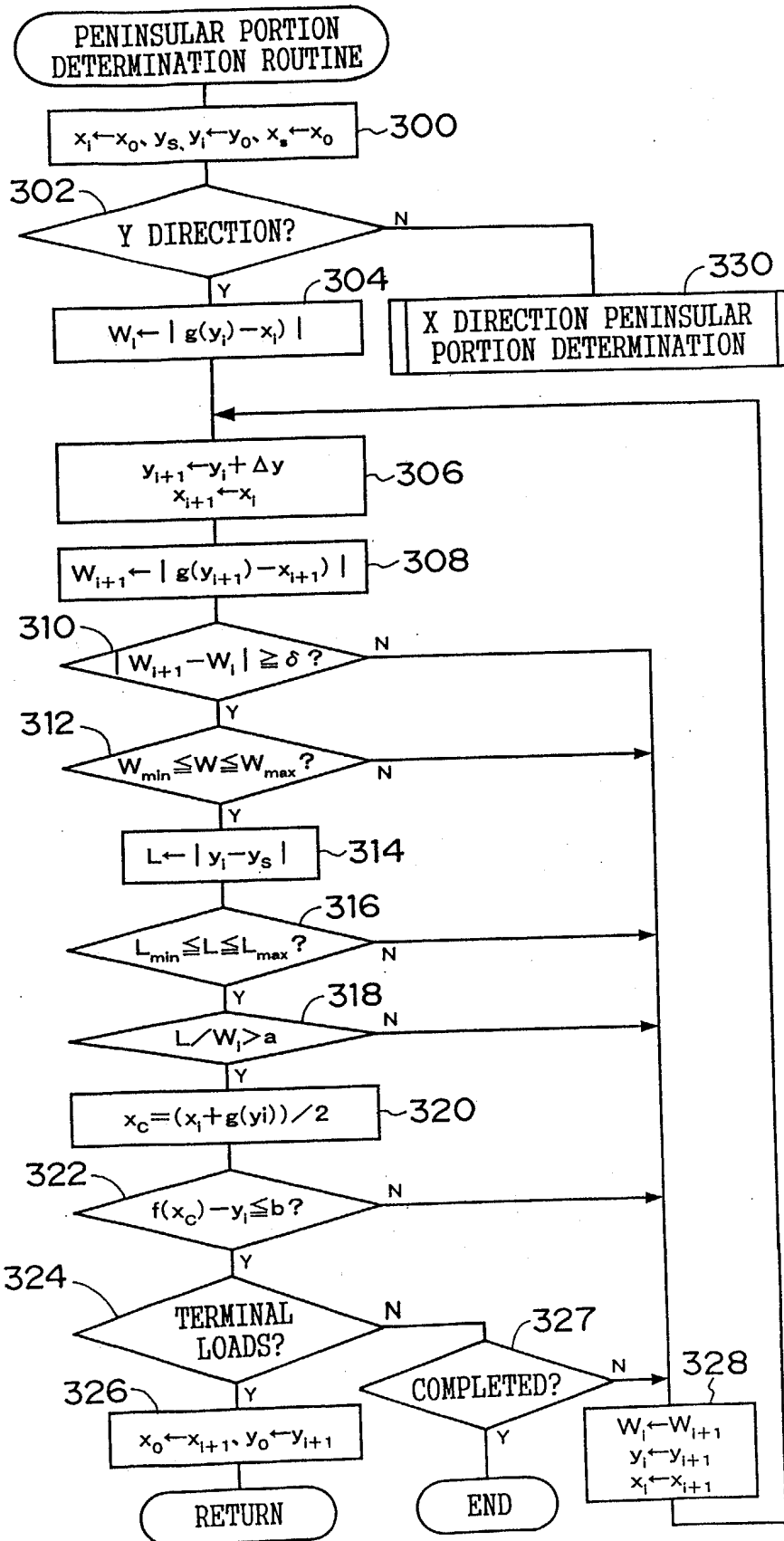
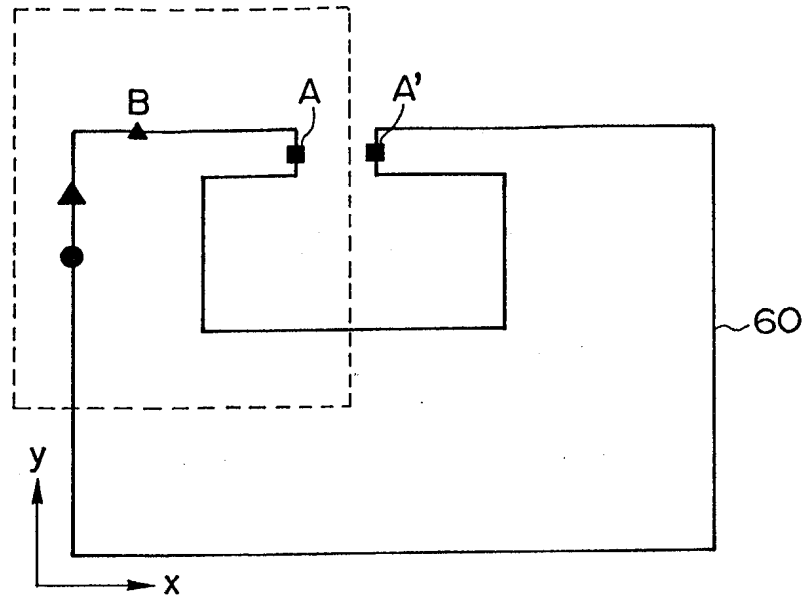


FIG. 18



F I G. 1 9



F I G. 20

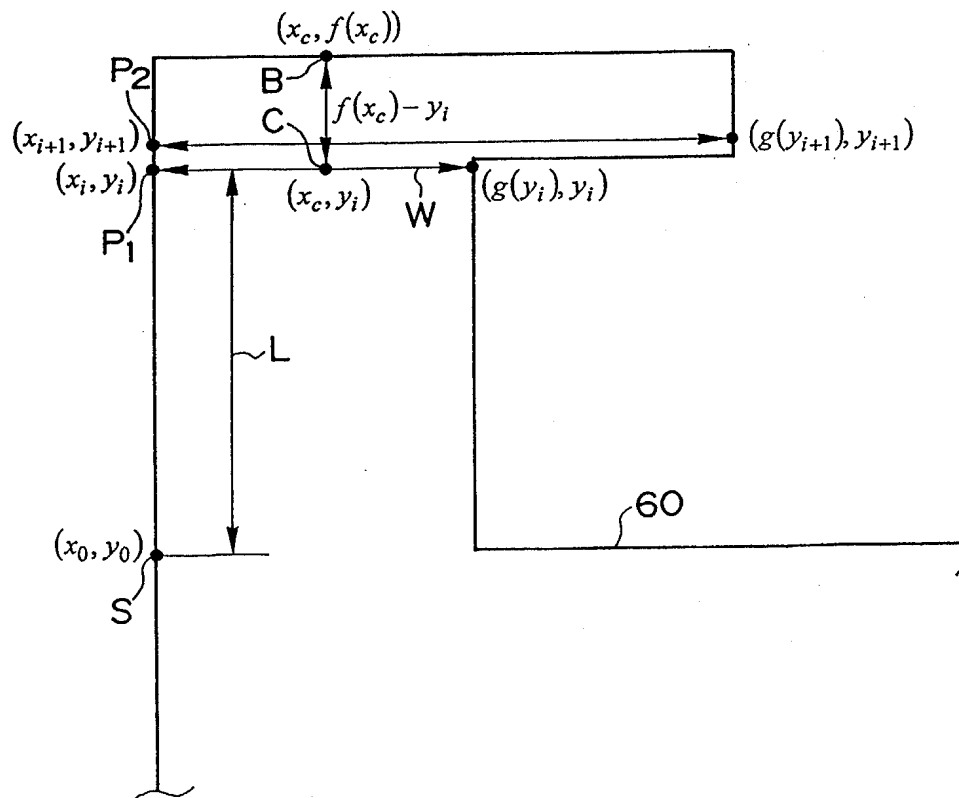
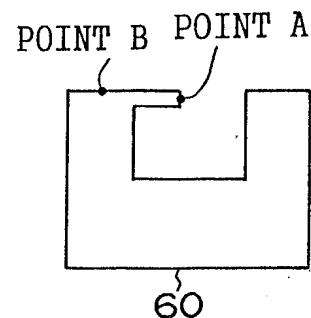
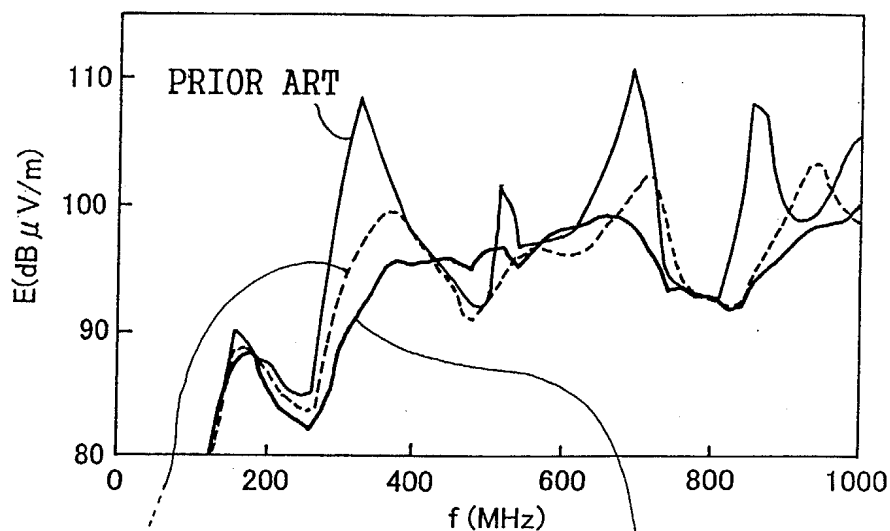


FIG. 21A

FIG. 21B



PRESENT INVENTION
(TERMINAL LOAD AT POINT A)

PRESENT INVENTION
(TERMINAL LOADS AT POINTS A AND B)

FIG. 22

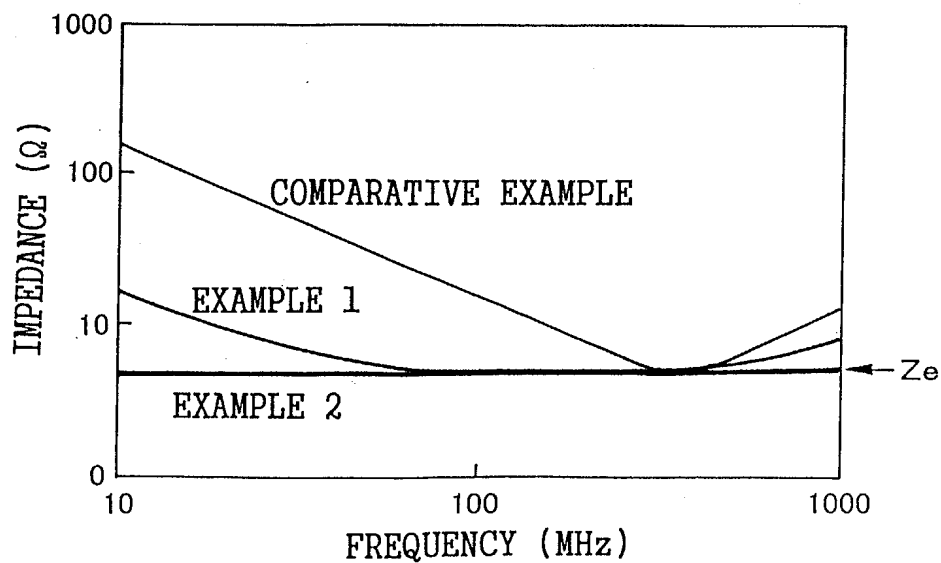


FIG. 23

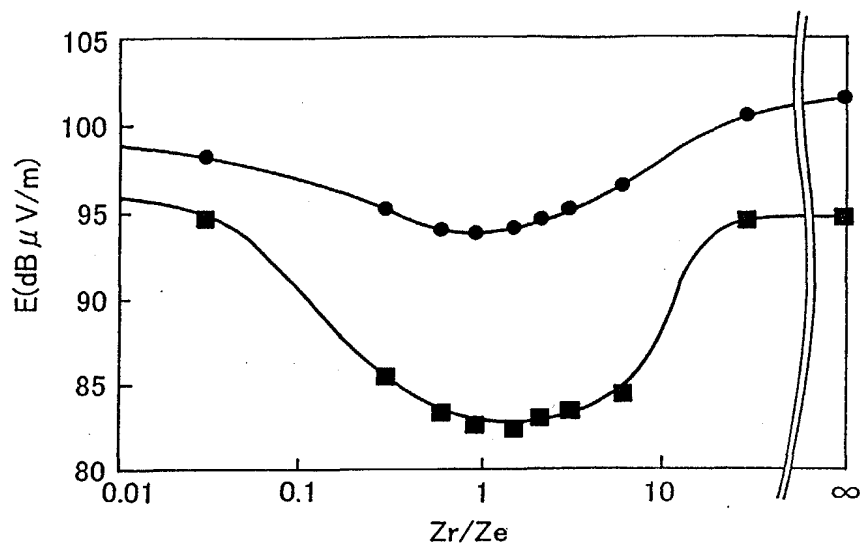


FIG. 24

